

Parameter	Rating	Units
Blocking Voltage	60	V <sub>P</sub>
Load Current	120	mA <sub>rms</sub> / mA <sub>DC</sub>
On-Resistance (max)	16	Ω
LED Current to operate	1	mA

### Features

- Designed for use in Security Systems Complying with EN50130-4
- 1500V<sub>rms</sub> Input/Output Isolation
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Immune to Radiated EM Fields
- Wave Solderable
- Tape & Reel Version Available
- Small 8-Pin SOIC Package

### Applications

- Security
  - Passive Infrared Detectors (PIR)
  - Data Signalling
  - Sensor Circuitry
- Instrumentation
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
- Medical Equipment—Patient/Equipment Isolation
- Aerospace
- Industrial Controls

### Description

The CPC2017N is a miniature device with two independent, normally-open (1-Form-A) solid state relays in an 8-pin SOIC package that employs optically coupled MOSFET technology to provide 1500V<sub>rms</sub> of input to output isolation.

Optically coupled outputs that use the patented OptoMOS architecture are controlled by a highly efficient infrared LED.

The CPC2017N uses IXYS Integrated Circuits Division's state of the art, double-molded, vertical construction packaging to produce one of the world's smallest relays. The CPC2017N offers substantial board space savings over the competitor's larger 8-pin SOIC relay.

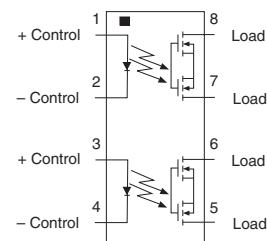
### Approvals

- UL Recognized Component: File E76270
- CSA Approval Pending
- EN/IEC 60950-1 Certified Component:  
TUV Certificate B 13 12 82667 003

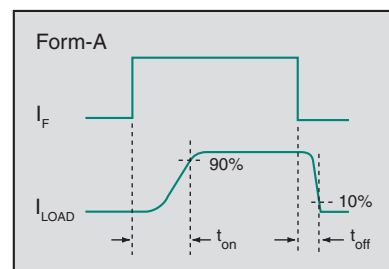
### Ordering Information

Part #	Description
CPC2017N	8-Pin SOIC (50/tube)
CPC2017NTR	8-Pin SOIC (2000/reel)

### Pin Configuration



### Switching Characteristics of Normally-Open (Form-A) Devices



### Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	60	V <sub>P</sub>
Reverse Input Voltage	5	V
Input Control Current Peak (10ms)	50	mA
	1	A
Total Power Dissipation <sup>1</sup>	600	mW
Isolation Voltage, Input to Output	1500	V <sub>rms</sub>
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

<sup>1</sup> Derate linearly 5mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

### Electrical Characteristics @ 25°C

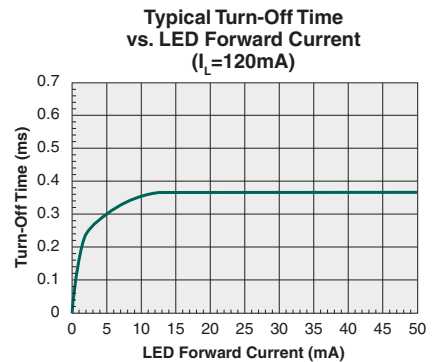
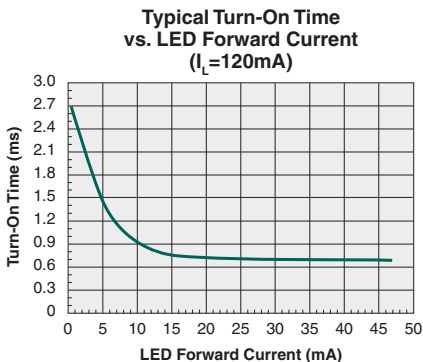
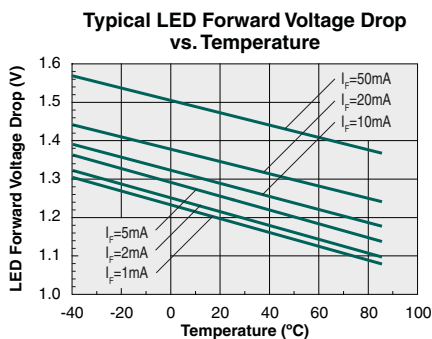
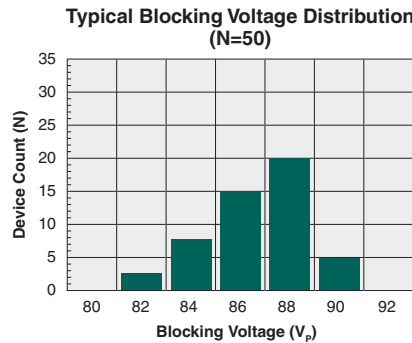
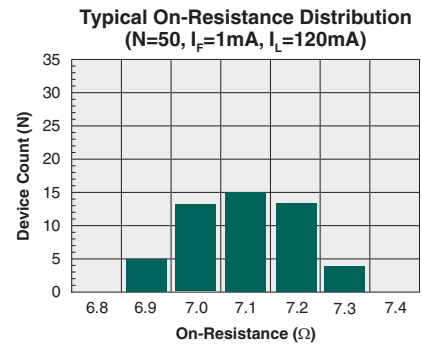
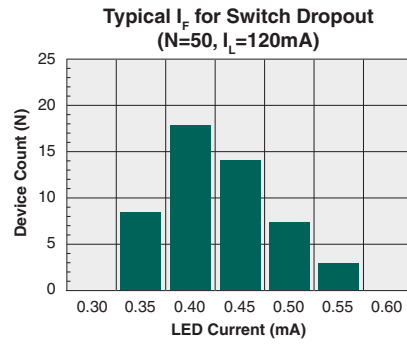
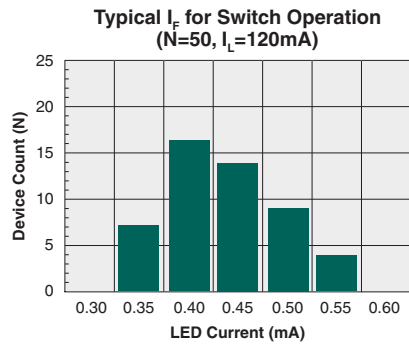
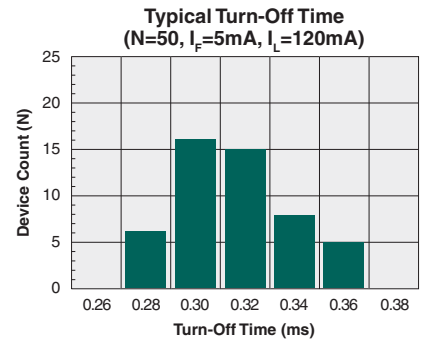
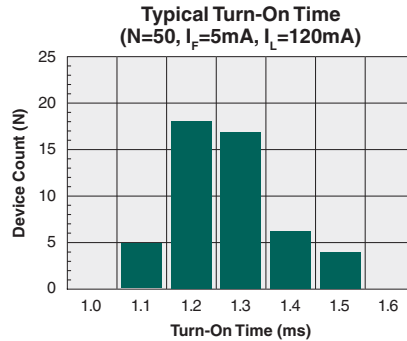
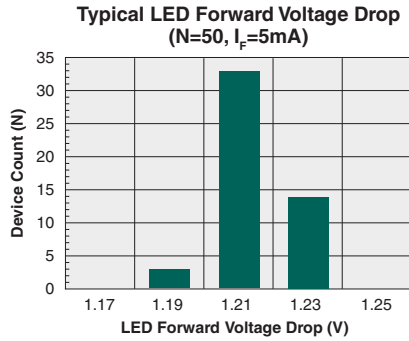
Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Output Characteristics</b>						
Load Current	I <sub>F</sub> =1mA	I <sub>L</sub>	-	-	120	mA <sub>rms</sub> / mA <sub>DC</sub>
Peak	t=10ms	I <sub>LPK</sub>	-	-	±350	mA <sub>p</sub>
On-Resistance <sup>2</sup>	I <sub>L</sub> =120mA	R <sub>ON</sub>	-	7.1	16	Ω
Off-State Leakage Current	V <sub>L</sub> =60V <sub>P</sub>	I <sub>LEAK</sub>	-	-	1	μA
<b>Switching Speeds</b>						
Turn-On	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>on</sub>	-	1.25	3	ms
Turn-Off		t <sub>off</sub>	-	0.45	3	
Output Capacitance	I <sub>F</sub> =0mA, V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	5	-	pF
Capacitance, Input to Output	V <sub>IO</sub> =0V, f=1MHz	C <sub>IO</sub>	-	1	-	pF
<b>Input Characteristics</b>						
Input Control Current to Activate <sup>3</sup>	I <sub>L</sub> =120mA	I <sub>F</sub>	-	0.40	1	mA
Input Control Current to Deactivate	-	I <sub>F</sub>	0.1	0.35	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μA

<sup>1</sup> Load current derates linearly from 120mA @ 25°C to 60mA @ 80°C, and must be derated for both poles operating simultaneously.

<sup>2</sup> Measurement taken within 1 second of on-time.

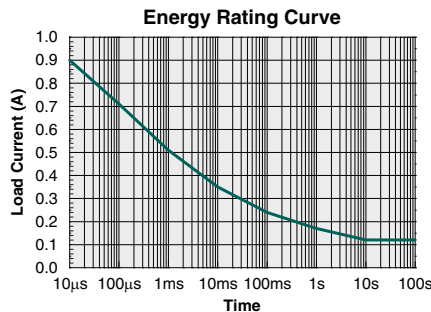
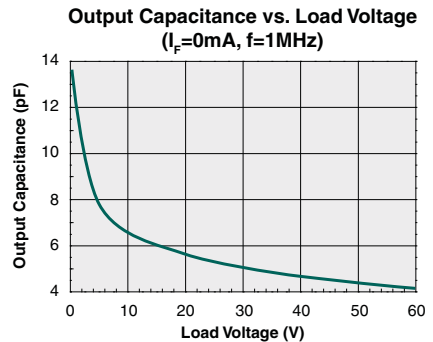
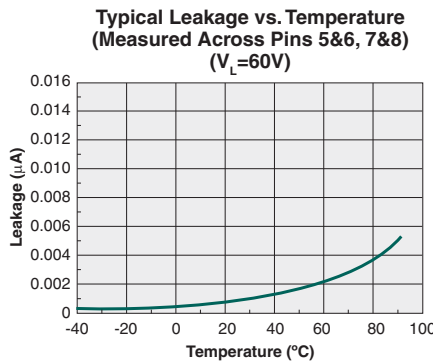
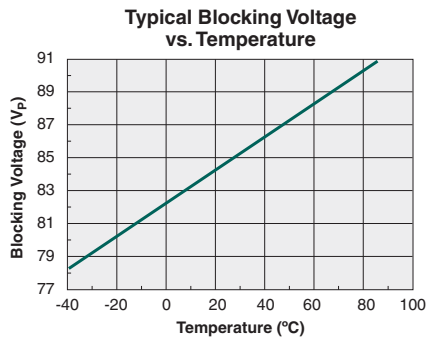
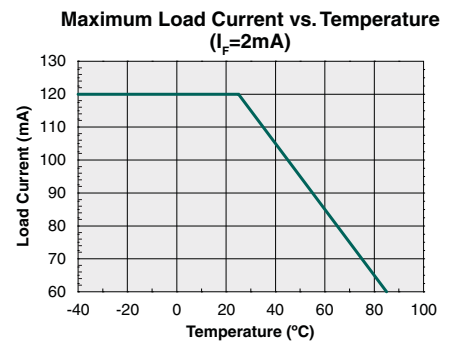
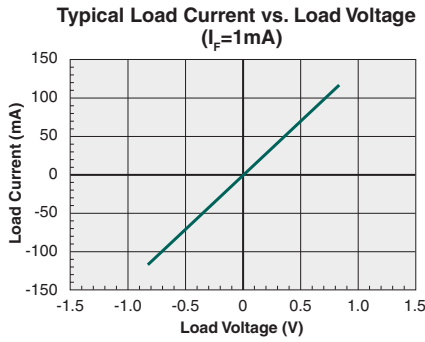
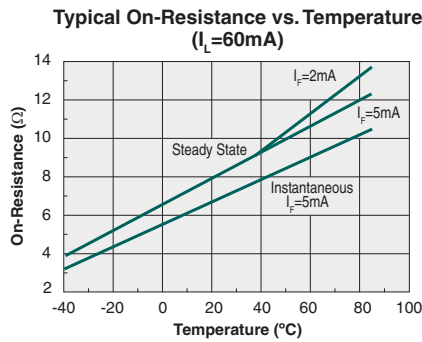
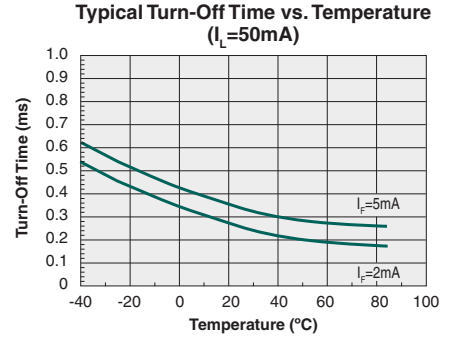
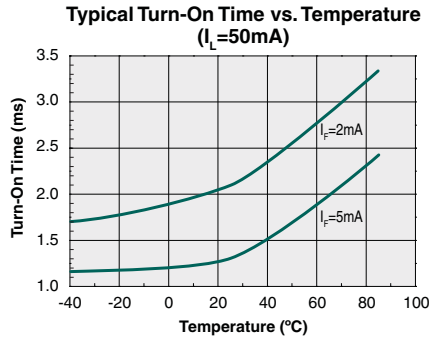
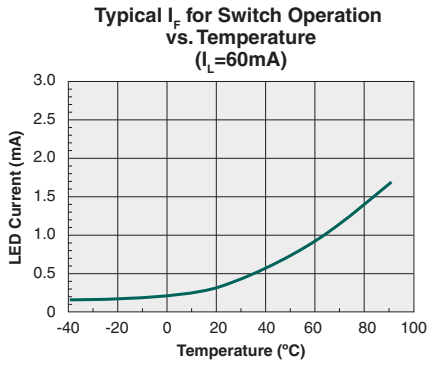
<sup>3</sup> For applications requiring high temperature operation (greater than 60°C) a minimum LED drive current of 3mA is recommended.

**PERFORMANCE DATA @ 25°C (Unless Otherwise Noted)\***



\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

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## Manufacturing Information

### Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
CPC2017N	MSL 3

### ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

### Soldering Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time	Maximum Reflow Cycles
CPC2017N	260°C for 30 seconds	3

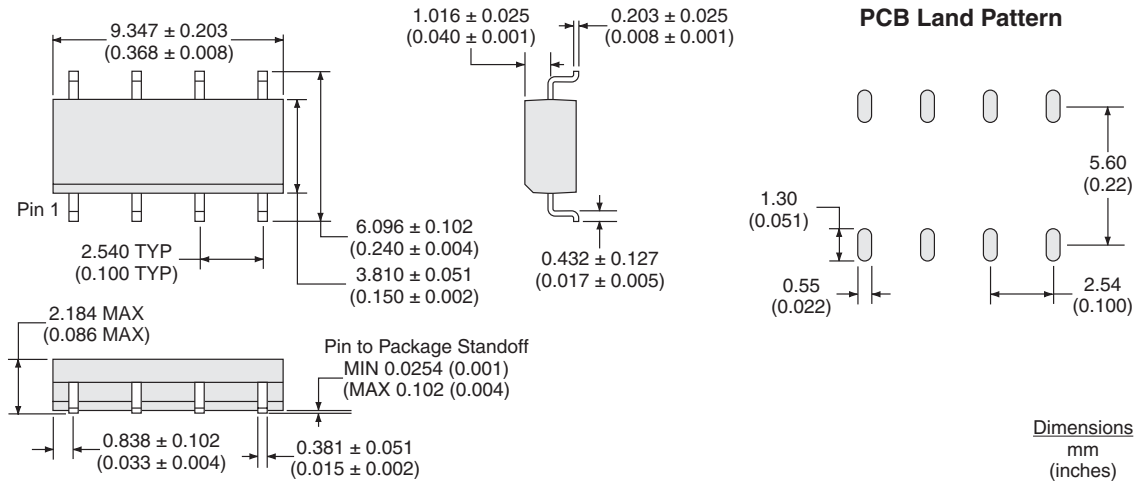
### Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after soldering processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

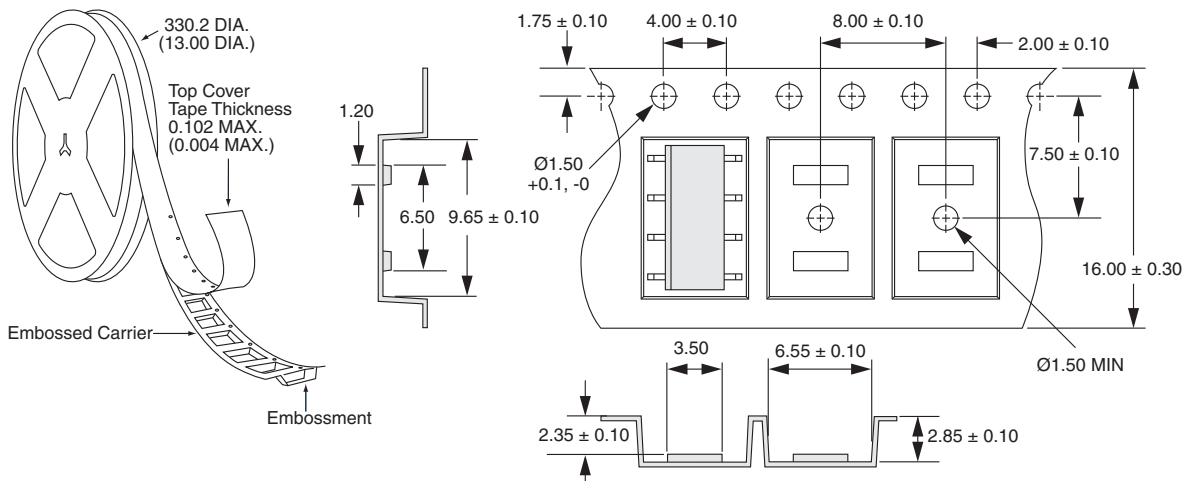


### MECHANICAL DIMENSIONS

#### CPC2017N



#### CPC2017NTR Tape & Reel



**NOTES:**

1. All dimensions in millimeters
2. 10 sprocket hole pitch cumulative tolerance  $\pm 0.20$ .
3. Carrier camber is within 1mm in 250mm.
4. Tape material : Black Conductive Polystyrene Alloy.
5. All dimensions meet EIA-481-C requirements.
6. Thickness :  $0.30 \pm 0.05$ mm.

For additional information please visit our website at: [www.ixysic.com](http://www.ixysic.com)

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